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(71) Applicant (for DE only): **PHILIPS INTELLECTUAL  
PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-  
damm 94, 20099 Hamburg (DE).

(71) Applicant (for all designated States except DE, US):  
**KONINKLIJKE PHILIPS ELECTRONICS N.V.**  
[NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven  
(NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **LOTZ, Andreas**

[DE/DE]; c/o Philips Intellectual Property & Standards  
GmbH, Weissshausstr. 2, 52066 Aachen (DE). **KOCH,**  
**Michael** [DE/DE]; c/o Philips Intellectual Property &  
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

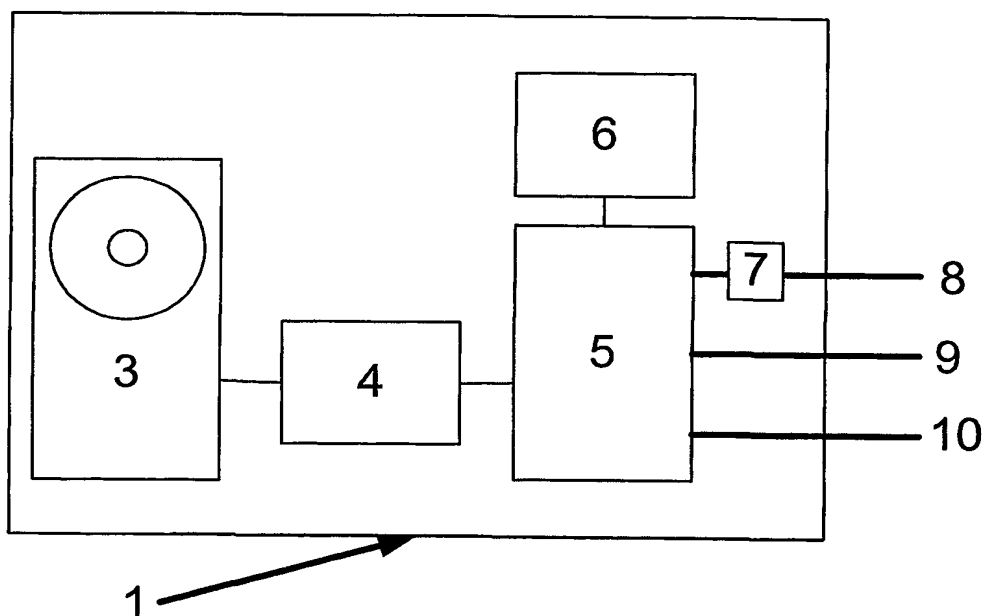
(74) Agent: **VOLMER, Georg**; Philips Intellectual Property &  
Standards GmbH, Weissshausstr. 2, 52066 Aachen (DE).

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(54) Title: **MODULE FOR READING A DATA CARRIER**



(57) Abstract: A module for reading a data carrier, with a processor arrangement and a memory arrangement, - wherein the module is designed for incorporation in a data processing device, and - wherein the processor arrangement is designed for storing an identification information associated with the data carrier and at least a start information in the memory arrangement when the reading of the data carrier is interrupted.

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